

5 W Low-Cost Packaged PHEMT GaAs Power FETs

FEATURES

- 5 W Typical Output Power at 2.45 GHz
- 11 dB Typical Linear Power Gain at 2.45 GHz
- High Linearity:
IP3 = 47 dBm Typical at 2.45 GHz
- High Power Added Efficiency:
Nominal PAE of 40 % at 2.45 GHz
- Suitable for High Reliability Application
- Breakdown Voltage:
 $BV_{DGO} \geq 18 \text{ V}$
- $L_g = 0.6 \mu\text{m}$, $W_g = 12 \text{ mm}$
- 100 % DC Tested
- Low Cost Ceramic Package

PHOTO ENLARGEMENT



DESCRIPTION

The TC2876 is packaged with the TC1806 Pseudomorphic High Electron Mobility Transistor (PHEMT) GaAs Power chip. The cu-based ceramic package provides excellent thermal conductivity for the GaAs FET. All devices are 100% DC tested to assure consistent quality. Typical applications include high dynamic range power amplifier for commercial applications including Cellular/PCS systems, and military high performance power amplifier.

ELECTRICAL SPECIFICATIONS ($T_A = 25 \text{ }^\circ\text{C}$)

Symbol	CONDITIONS	MIN	TYP	MAX	UNIT
P_{1dB}	Output Power at 1dB Gain Compression Point, $f = 2.45\text{GHz}$ $V_{DS} = 8 \text{ V}$, $I_{DS} = 1300 \text{ mA}$	36	36.5		dBm
G_L	Linear Power Gain, $f = 2.45\text{GHz}$ $V_{DS} = 8 \text{ V}$, $I_{DS} = 1300 \text{ mA}$		11		dB
IP3	Intercept Point of the 3 rd -order Intermodulation, $f = 2.45\text{GHz}$ $V_{DS} = 8 \text{ V}$, $I_{DS} = 1300 \text{ mA}$, $*P_{SCL} = 23 \text{ dBm}$		47		dBm
PAE	Power Added Efficiency at 1dB Compression Power, $f = 2.45\text{GHz}$		40		%
I_{DSS}	Saturated Drain-Source Current at $V_{DS} = 2 \text{ V}$, $V_{GS} = 0 \text{ V}$		3		A
g_m	Transconductance at $V_{DS} = 2 \text{ V}$, $V_{GS} = 0 \text{ V}$		2000		mS
V_p	Pinch-off Voltage at $V_{DS} = 2 \text{ V}$, $I_D = 24 \text{ mA}$		-1.7**		Volts
BV_{DGO}	Drain-Gate Breakdown Voltage at $I_{DGO} = 6 \text{ mA}$	18	22		Volts
R_{th}	Thermal Resistance		3.5		$^\circ\text{C/W}$

Note: * P_{SCL} : Output Power of Single Carrier Level.

** For the tight control of the pinch-off voltage range, we divide TC2876 into 3 model numbers to fit customer design requirement
 (1)TC2876P1519 : $V_p = -1.5\text{V to } -1.9\text{V}$ (2)TC2876P1620 : $V_p = -1.6\text{V to } -2.0\text{V}$ (3)TC2876P1721 : $V_p = -1.7\text{V to } -2.1\text{V}$
 If required, customer can specify the requirement in purchasing document. For special V_p requirement, please contact factory for details.

ABSOLUTE MAXIMUM RATINGS (T_A=25 °C)

Symbol	Parameter	Rating
V _{DS}	Drain-Source Voltage	12 V
V _{GS}	Gate-Source Voltage	-5 V
I _{DS}	Drain Current	I _{DSS}
P _{in}	RF Input Power, CW	27 dBm
P _T	Continuous Dissipation	19 W
T _{CH}	Channel Temperature	175 °C
T _{STG}	Storage Temperature	- 65 °C to +175 °C

RECOMMENDED OPERATING CONDITION

Symbol	Parameter	Rating
V _{DS}	Drain to Source Voltage	8 V
I _D	Drain Current	1300 mA

HANDLING PRECAUTIONS:

The user must operate in a clean, dry environment. Electrostatic Discharge (ESD) precautions should be observed at all stages of storage, handling, assembly, and testing. The static discharge must be less than 300V.

OUTLINE DIMENSIONS (in inch)

